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		ing) Art Unit	2817	, C
		Examiner Name	Lee, B.	
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This collection of information is required by 37 CFR 1.5. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, Washington, DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, Washington, DC 20231.



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

ALLISON et al.

Serial No. 09/607,604

Filed: 06/30/2000

For: MULTI-BIT PHASE SHIFTERS USING

MEM RF SWITCHES

Art Unit: 2817

Examiner: Lee, B.

Date: August 1, 2003

RESPONSE TO FINAL REJECTION

Commissioner for Patents P.O. Box 1450 Arlington, VA

Dear Sir:

In response to the Office Action mailed June 5, 2003, kindly amend the application as follows:

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks/Arguments begin on page 6 of this paper.

IN THE CLAIMS

This listing of claims will replace all prior versions, and listings, of claims in the application: Claims 1-4 (canceled)

5. (Previously Amended) The array of Claim 27 wherein said MEM switches are single-pole-single-throw (SPST) switches including an armature for opening and closing the RF signal path through the switch, and a control signal path, and wherein the control signals are isolated from the RF signal path.